



MITS

MADANAPALLE INSTITUTE OF TECHNOLOGY & SCIENCE

(Deemed to be University under section 3 of UGC Act, 1956)

EXAMINATION SECTION

ACADEMIC YEAR: 2025-26

Time Table - M.Tech. I Year II Semester - I Mid Term Test - April 2026

TIMINGS: FN: 11:00 A.M to 01:00 PM

AN: 03:00 P.M to 05:00 PM

Date/Day	Session	Computer Science and Engineering (CSE)	VLSI Design and Embedded Systems (VES)
16.04.2026 (Thursday)	FN	Advanced Machine Learning-25MBCSETC03	CMOS Analog IC Design-25MBVESTC03
	AN	Advanced Data Visualization Techniques- 25MBCSETC04	Embedded System Design-25MBVESTC04
17.04.2026 (Friday)	FN	Cyber Security-25MBCSEDC09	Pattern Recognition and Machine Learning- 25MBVESDC07
	AN	Responsible AI-25MBCSEDC10	High Speed VLSI Design-25MBVESDC12

Dated: 31.03.2026

CONTROLLER OF EXAMINATIONS

CONTROLLER OF EXAMINATIONS
Madanapalle Institute of Technology & Science
(Deemed to be University)
MADANAPALLE - 517 325, A. P.

To be read in all the M. Tech-(CSE, VES) I Year II Semester Classrooms

Copy to The File Notice Board HoD-CSE HoD-ECE Transport Incharge

Note: HoD is requested to circulate among the faculty members concerned